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## PATENT ABSTRACTS OF JAPAN

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**H04L 27/38****H04L 1/00****H04L 27/22**(21) Application number: **05102695**(71) Applicant: **TOSHIBA CORP**(22) Date of filing: **28.04.93**(72) Inventor: **OTO HIDEKI****(54) DIGITAL TRANSMITTER-RECEIVER****(57) Abstract:**

**PURPOSE:** To provide the digital transmitter-receiver which can optimally control fixed degradation at the time of low CN or adjacent transmission.

**CONSTITUTION:** This digital transmitter-receiver calculates a transmission error rate from the state of error correction at an error correcting circuit 8 by using a control part 10. An input C/N is calculated from that value, and either the cut-off frequency of base band filters 51 and 52 or the time constant of a control signal to an oscillator 44 for carrier reproduction is variably controlled so as to optimize the fixed degradation. Thus, the error rate at the low C/N is optimized and the fixed degradation is reduced so that a wide dynamic range can be obtained.

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